



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Bhattacharya et al.

Serial No.: 09/896,071

For: Process for Automated Generation of Design-Specific Complex
Functional Blocks to Improve Quality of Synthesized Digital
Integrated Circuits in CMOS

Filed: 29 JUN 2001

Examiner: Thuan V. Do

Art Unit: 2825

Confirmation No.: 9529

Customer No.: 27,623

Attorney Docket No.: 162.7106USU

AMENDMENT

**Mail Stop Amendment
COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450**

Dear Sir:

Applicants are submitting the present document in response to an Office Action dated 7 JUN 2005 (hereinafter "the Office Action") for the above-noted application.

Please amend the application as follows:

Amendments to Claims begin on page 2 of this paper.

Remarks begin on page 9 of this paper.